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**TRANSMITTAL
FORM**

(to be used for all correspondence after initial filing)

TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	Patent#: 7,194,570
	Filing Date	Issued: March 20, 2007
	First Named Inventor	Zahra Claude
	Art Unit	2188
	Examiner Name	D. Y. Kim
Total Number of Pages in This Submission	Attorney Docket Number	S1022.81101US00

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input type="checkbox"/> Amendment/Reply	<input type="checkbox"/> Petition	<input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Terminal Disclaimer	<input checked="" type="checkbox"/> Other Enclosure(s) (please Identify below):
<input checked="" type="checkbox"/> Request for Certificate of Correction	<input type="checkbox"/> Request for Refund	Return Receipt Postcard
<input checked="" type="checkbox"/> Certificate of Correction	<input type="checkbox"/> CD, Number of CD(s) _____	
<input checked="" type="checkbox"/> Page 10 Application as Filed, Page 4 of 08/21/06 Amendment	<input type="checkbox"/> Landscape Table on CD	
<input checked="" type="checkbox"/> Column 6 of U.S. Patent No. 7,194,570		
<input type="checkbox"/> Reply to Missing Parts under 37 CFR 1.52 or 1.53		

Remarks

**Certificate
APR 09 2007
of Correction****SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm Name	WOLF, GREENFIELD & SACKS, P.C.		
Signature	<i>William R. McClellan</i>		
Printed name	William R. McClellan		
Date	April 3, 2007	Reg. No.	29,409

Certificate of Mailing Under 37 CFR 1.8(a)

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as First Class Mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

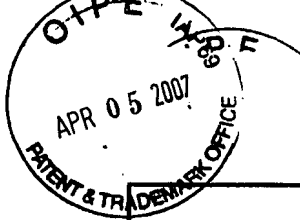
Dated: April 3 2007

Signature:

Gail Driscoll

(Gail Driscoll)

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TRANSMITTAL FORM

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Dated: April 3 2007

Signature: *Gail Driscoll* (Gail Driscoll)

APR 09 2007



Docket No.: S1022.81101US00
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Zahra Claude and Yannick Teglia
Serial No.: 10/784,430 Patent No. 7,194,570
Filed: February 23, 2004 Issued: March 20, 2007
Patent No.: 7,194,570
For: METHOD AND DEVICE FOR SELECTING THE OPERATING
MODE OF AN INTEGRATED CIRCUIT

Examiner: D. Y. Kim
Art Unit: 2188 Confirmation No.: 2548

Certificate of Mailing Under 37 CFR 1.8(a)

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Dated: April 3, 2007


Gail Driscoll

**REQUEST FOR CERTIFICATE OF CORRECTION
PURSUANT TO 37 CFR 1.322**

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentees noted a typographical error which should be corrected.

In Claim 21, line 66 the word "and" has been repeated. This error was not in the application as filed nor was it in the amendment filed August 21, 2006.

In support of this request, Patentees enclose a highlighted copy of the pertinent page of the application as filed (page 10) and the pertinent page of the amendment filed August 21, 2006 (page 4) and column 6 of U.S. Patent No. 7,194,570.

Patentees respectfully submit that, since the error for which a Certificate of Correction is sought was the result of Patent Office mistake, no fee is due. However, if the Examiner deems a fee necessary, the fee may be charged to the account of the undersigned, Deposit Account No. 23/2825.

APR 09 2007

Transmitted herewith is a proposed Certificate of Correction, PTO Form PTO/SB/44, effecting such amendment. Patentees respectfully solicit the granting of the requested Certificate of Correction.

Dated: April 3, 2007

Respectfully submitted,

By William R. McClellan

William R. McClellan

Registration No.: 29,409

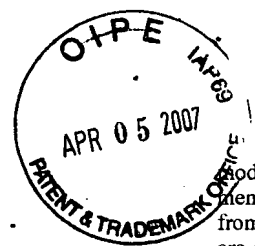
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mode may correspond to a value of the programmable memory which belongs to a range closer to or more remote from the value of the ROM, granting the user rights which are all the greater as the range is close to the value of the ROM.

Moreover, the user mode may be divided into several operating sub-modes, the selection of which may be conventionally controlled by software or by an activation of specific pins.

The present invention has been described in relation with an integrated circuit having its functional block switching to the user mode as soon as the selection signal generated by the selection device is inactive, but those skilled in the art will readily adapt the present invention to an integrated circuit having its functional block switching to the user mode at the first powering-on which follows the activation of the selection signal.

Finally, the selection of the number and of the size of the words stored in the memories as well as of the threshold from which it is considered that there is an identity between them is within the abilities of those skilled in the art according to the application and especially to the reliability of the non-volatile character of the programmable memory. For example, in an application to smart cards, two predetermined values of one byte are used and the threshold is of six bits per byte (75%). The size of a word in the meaning of the present invention then is of one bit. A byte-to-byte identity may also be provided with a threshold of six bytes out of eight.

Such alterations, modifications, and improvements are intended to be part of this disclosure, and are intended to be within the spirit and the scope of the present invention. Accordingly, the foregoing description is by way of example only and is not intended to be limiting. The present invention is limited only as defined in the following claims and the equivalents thereto.

What is claimed is:

1. A device for selecting an operating mode of an integrated circuit, comprising:
 - a ROM storing at least one predetermined value formed of data words;
 - a non-volatile programmable memory controllable to store said predetermined value;
 - a comparator indicating how many data words of the value stored in the programmable memory are identical to the data words of the predetermined value; and
 - control means deactivating a selection signal for selecting the operating mode when the number of identical words is greater than a predetermined threshold.
2. The device of claim 1, wherein the value stored in the programmable memory varies with time and the device comprises writing means for, when the selection signal is deactivated, rewriting at intervals the predetermined value into the programmable memory.
3. The device of claim 2, wherein said writing means are provided to rewrite the predetermined value into the programmable memory upon each powering-on of the integrated circuit when the selection signal is deactivated.
4. The device of claim 2, wherein said writing means, the comparator, and the control means are implemented in software form by a microprocessor.
5. The device of claim 1, wherein said threshold corresponds to from 70% to 90% of the number of compared words.
6. An integrated circuit comprising the device of claim 1 and further comprising a functional block provided to operate in a first operating mode when the selection signal is

activated and to operate in a second operating mode otherwise, the functional block enabling controlling the programmable memory to store the predetermined value.

7. A method for selecting an operating mode of an integrated circuit between a reserved mode and a user mode, comprising:

- a) determining how many data words of a value stored in a non-volatile programmable memory of the circuit are identical to data words of at least one predetermined value stored in a ROM of the circuit; and
- b) selecting the user mode when the number of identical words is greater than a predetermined threshold.

8. The method of claim 7, wherein said threshold corresponds to from 70% to 90% of the number of words of the predetermined value.

9. A method for locking the user mode selected by the implementation of the method of claim 7, comprising preventing storage of a value other than the predetermined value in the programmable memory.

10. The user mode locking method of claim 9, wherein the predetermined value is copied into the programmable memory at least at each powering-on of the circuit.

11. A device for selecting an operating mode of an integrated circuit, comprising:

- a read-only memory configured to store at least one predetermined value including data words;
- a non-volatile programmable memory configured to store the predetermined value in response to a control signal;
- a comparator configured to indicate a number of data words in the programmable memory that are identical to the data words of the predetermined value; and
- a control circuit configured to deactivate a mode select signal when the number of identical words indicated by the comparator is greater than a predetermined threshold.

12. A device as defined in claim 11, wherein the control circuit is configured to enable rewriting of the programmable memory when the mode select signal is inactive.

13. A device as defined in claim 12, wherein the control circuit is configured to enable rewriting of the programmable memory on power up.

14. A device as defined in claim 12, wherein the control circuit is configured to enable rewriting of the programmable memory periodically.

15. A device as defined in claim 11, wherein the programmable memory comprises a ferromagnetic memory.

16. A device as defined in claim 11, wherein the predetermined threshold is in a range from 70% to 90% of the number of words of the predetermined value stored in the read-only memory.

17. A device as defined in claim 11, wherein the control circuit is configured to control two or more reserved modes.

18. A device as defined in claim 11, wherein the control circuit is configured to control two or more user modes.

19. A device as defined in claim 11, wherein the control circuit is configured to enable a user mode upon a first power up of the integrated circuit after deactivation of the mode select signal.

20. A device as defined in claim 11, wherein the comparator requires byte-to-byte identity of the data words in the programmable memory and the data words of the predetermined value.

21. A device as defined in claim 11, wherein the comparator requires bit-to-bit identity of the data words in the programmable memory and the data words of the predetermined value.



13. A device as defined in claim 12, wherein the control circuit is configured to enable rewriting of the programmable memory on power up.

14. A device as defined in claim 12, wherein the control circuit is configured to
5 enable rewriting of the programmable memory periodically.

15. A device as defined in claim 11, wherein the programmable memory comprises a ferromagnetic memory.

10 16. A device as defined in claim 11, wherein the predetermined threshold is in a range from 70% to 90% of the number of words of the predetermined value stored in the read-only memory.

15 17. A device as defined in claim 11, wherein the control circuit is configured to control two or more reserved modes.

18. A device as defined in claim 11, wherein the control circuit is configured to control two or more user modes.

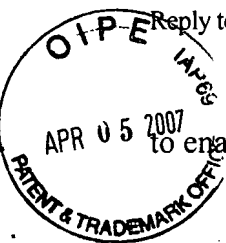
20 19. A device as defined in claim 11, wherein the control circuit is configured to enable a user mode upon a first power up of the integrated circuit after deactivation of the mode select signal.

25 20. A device as defined in claim 11, wherein the comparator requires byte-to-byte identity of the data words in the programmable memory and the data words of the predetermined value.

30 21. A device as defined in claim 11, wherein the comparator requires bit-to-bit identity of the data words in the programmable memory and the data words of the predetermined value.

22. An integrated circuit comprising:

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13. (Original) A device as defined in claim 12, wherein the control circuit is configured to enable rewriting of the programmable memory on power up.

14. (Original) A device as defined in claim 12, wherein the control circuit is configured to enable rewriting of the programmable memory periodically.

15. (Original) A device as defined in claim 11, wherein the programmable memory comprises a ferromagnetic memory.

16. (Original) A device as defined in claim 11, wherein the predetermined threshold is in a range from 70% to 90% of the number of words of the predetermined value stored in the read-only memory.

17. (Original) A device as defined in claim 11, wherein the control circuit is configured to control two or more reserved modes.

18. (Original) A device as defined in claim 11, wherein the control circuit is configured to control two or more user modes.

19. (Original) A device as defined in claim 11, wherein the control circuit is configured to enable a user mode upon a first power up of the integrated circuit after deactivation of the mode select signal.

20. (Original) A device as defined in claim 11, wherein the comparator requires byte-to-byte identity of the data words in the programmable memory and the data words of the predetermined value.

21. (Original) A device as defined in claim 11, wherein the comparator requires bit-to-bit identity of the data words in the programmable memory and the data words of the predetermined value.

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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**Page 1 of 1

PATENT NO. : 7,194,570
APPLICATION NO. : 10/784,430
ISSUE DATE : March 20, 2007
INVENTOR(S) : Zahra Claude and Yannick Teglia

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 21, col. 6, line 66 should read:
--programmable memory and the data words of the--

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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MAY 9 2007